

Appendix B

This appendix shows the Simulink block diagram schemes used to simulate the designed observers, including in each case the closed loop FOC scheme for PMSMs.

Figure B.1:
Simulink block diagram
involving currents and
speed controllers (FOC),
a PMSM model and the
adaptive observer.

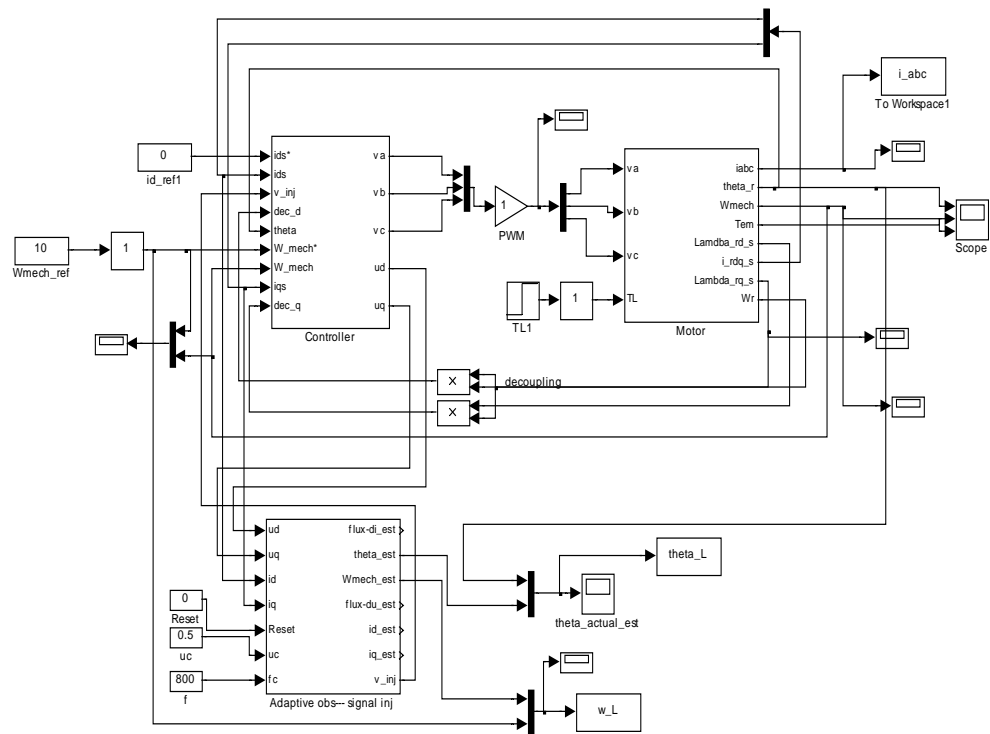


Figure B.2: Simulink block diagram involving currents and speed controllers (FOC), a PMSM model and the modified voltage model observer.

